

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yasuhiro OKAMOTO et al.

Title: FIELD-EFFECT TRANSISTOR

Appl. No.: Unassigned

Filing Date: 6/14/2005

Examiner: Unassigned

Art Unit: Unassigned

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.56

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith on Form PTO/SB/08 is a listing of documents known to Applicants in order to comply with Applicants' duty of disclosure pursuant to 37 CFR §1.56.

A copy of each non-U.S. patent document and each non-patent document is being submitted to comply with the provisions of 37 CFR §1.97 and §1.98.

The submission of any document herewith, which is not a statutory bar, is not intended as an admission that such document constitutes prior art against the claims of the present application or that such document is considered material to patentability as defined in 37 CFR §1.56(b). Applicants do not waive any rights to take any action which would be appropriate to antedate or otherwise remove as a competent reference any document which is determined to be a *prima facie* art reference against the claims of the present application.

10/538739

Atty. Dkt. No. 029437-0108

JC17 Rec'd PCT/PTO 14 JUN 2005

TIMING OF THE DISCLOSURE

The listed documents are being submitted in compliance with 37 CFR §1.97(b), within three (3) months of the date of entry of the national stage as set forth in 37 CFR §1.491.

RELEVANCE OF EACH DOCUMENT

Any document listed on the attached PTO/SB/08 was cited as being relevant during the prosecution of the corresponding International application. A copy of the International Search Report is attached setting forth the portion of each document considered relevant by the examiner. An English-language Abstract of each foreign-language document has been provided.

Applicants respectfully request that each listed document be considered by the Examiner and be made of record in the present application and that an initialed copy of Form PTO/SB/08 be returned in accordance with MPEP §609.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 CFR §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741.

Respectfully submitted,

Date June 14, 2005

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Substitute for form 1449B/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Application Number	Unassigned 10/538739
Date Submitted: June 14, 2005		Filing Date	6/14/2005
(use as many sheets as necessary)		First Named Inventor	Yasuhiro OKAMOTO
Sheet	1	Group Art Unit	Unassigned
		Examiner Name	Unassigned
		Attorney Docket Number	029437-0108

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Documents	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ⁵ (if known)				
	A2	JP	09-307097	A	Sony Corp.	11/28/1997		A
	A3	JP	2000-100831	A	NEC Corp.	04/07/2000		A
	A4	JP	2000-323495	A	Sony Corp.	11/24/2000		A
	A5	JP	2001-189324	A	Ricoh Co. Ltd.	07/10/2001		A
	A6	JP	2002-359256	A	Fujitsu Ltd.	12/13/2002		A

NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ⁶
	A7	ANDO et al., "A 110-W AlGaN/GaN Heterojunction FET on Thinned Sapphire Substrate," IEDM 01, 2001, pp. 381-384.	
	A8	LI et al., "High breakdown voltage GaN HFET with field plate," Electronics Letters, February 1, 2001, Vol. 37, No. 3, pp. 196-197.	
	A9	MISHRA et al., "AlGaN/GaN HEMTs – An Overview of Device Operation and Applications," Proceedings of the IEEE, June 2002, vol. 90, No. 6, pp. 1022-1031.	
	A10	TAN et al., "The Effect of Dielectric Stress on the Electrical Characteristics of AlGaN/GaN Heterostructure Field-Effect Transistors (HFETs)," The 10 th IEEE International Symposium on Electron Devices for Microwave and Optoelectronic Applications, November 2002, pp. 130-135.	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.